




ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

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U.S. **UTILITY** Patent Application

<p>  </p> <p> O.I.P.E. SCANNED  Q.A.  </p>	<p>PATENT DATE</p>
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73 Jc906

APPLICATION NO. 09/625116	CONT/PRIOR D F	CLASS 257 361	SUBCLASS 5C	ART UNIT 2811 2536	EXAMINER Boris Senarso
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Isabelle Claverie

Protection of the logic well of a component including an integrated MOS power transistor

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
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